
Features

- Adjustable and Retriggerable Tracking Time
- Window Monitoring for Sensor Input
- Enable Input for Triggering
- Internal Noise Suppression (40 ms) and Retrigger Blocking (640 ms)
- Two- or Three-wire Applications

Applications

- Motion Detectors
- Touch Sensors
- Timers

Description

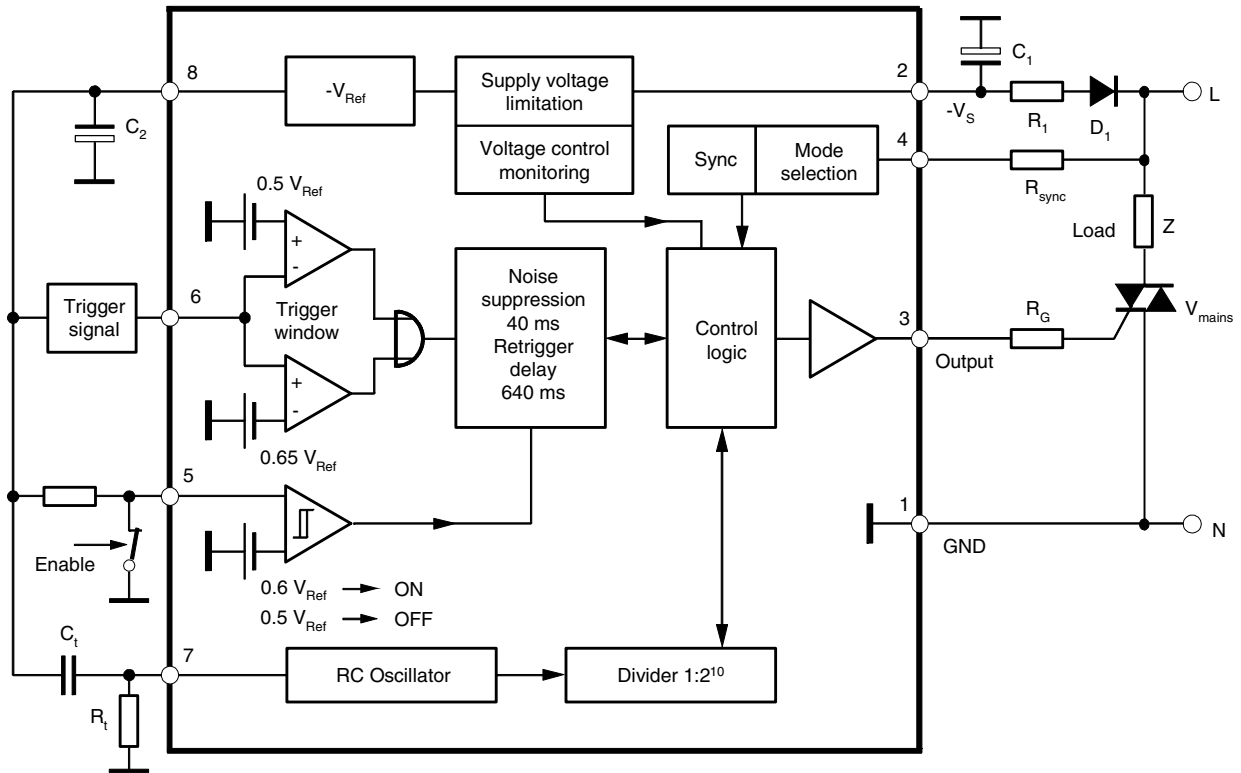
The timer control circuit U2100B uses bipolar technology. It has different mode selections (zero voltage switch, phase control and relay control). The output stage is triggered according to input conditions. It can be used in triac application for two- or three-wire systems as a power switch.



Timer Control for Triac and Relay

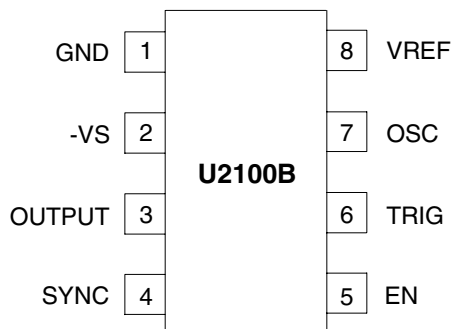
U2100B

Figure 1. Block Diagram with External Circuit



Pin Configuration

Figure 2. Pinning DIP8/SO8



Pin Description

Pin	Symbol	Function
1	GND	Reference point
2	-VS	Supply voltage
3	OUTPUT	Driver output
4	SYNC	Synchronization and mode selection
5	EN	Enable
6	TRIG	Input trigger signal
7	OSC	RC oscillator
8	VREF	Reference voltage

General Description

The monostable integrated power-control circuit U2100B can be used according to the mode selection in relay or triac applications. In addition, it can be used in triac applications for two-wire systems as power switch (the load in series to the switch), where the supply voltage for the control unit is gained from the remaining phase angle ($\alpha_{\min}$ operation).

Figure 3. Two-wire Circuit

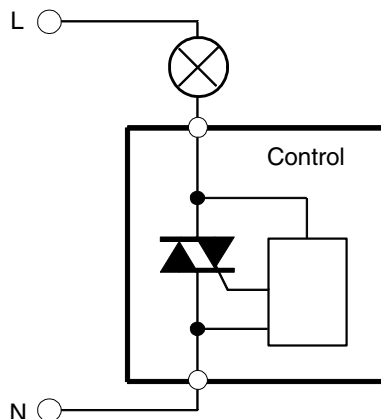
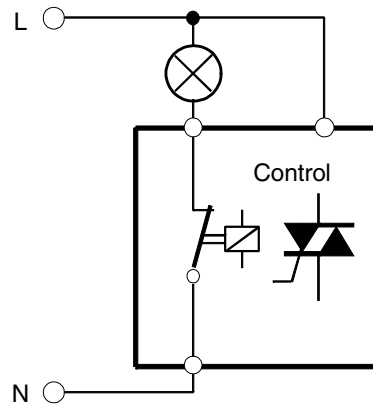


Figure 4. Three-wire Circuit



For three-wire switch systems, two operational modes are possible:

- Zero voltage switch operation for triac control
- Static operation for relay control

Mode Selection Pin 4 and Supply Voltage Pin 2

The operational modes can be selected by the external voltage at the synchronous input pin 4 (clamping). The mode selection determines the current requirement of the relay's or triac's driver stage and hence the selection of the supply voltage.

Zero Voltage Switch Operation (Figure 5)

Selection condition:

V_4 = internal synchronous limitation, without external clamping

$$R_1 \approx 0.85 \frac{V_M - V_S}{2 I_{tot}}$$

$$I_{tot} = I_S + I_p + I_X$$

where:

I_S = Supply current of the IC without load

I_p = Average trigger current I_G

I_X = External circuit current requirement

V_M = Mains voltage

Required firing pulse width t_p

$$t_p = \frac{2}{\omega} \arcsin \left(\frac{I_L \times V_M}{P \times \sqrt{2}} \right)$$

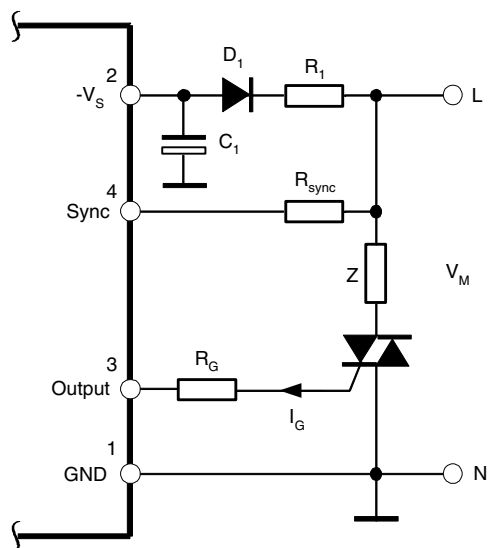
where:

I_L = Triac latching current

P = Power at load Z

$$R_{sync}[k\Omega] \approx \frac{V_M[V] \times \sqrt{2} \sin(\omega \times t_p[s]) - 0.7}{1.8 \times 10^{-2}} - 176$$

Figure 5. Zero Voltage Switch Operation



DC Operation

Selection condition (Figure 6):

$$+V_4 = 6.1 \text{ V} \quad -V_4 = \text{internal limitation}$$

where:

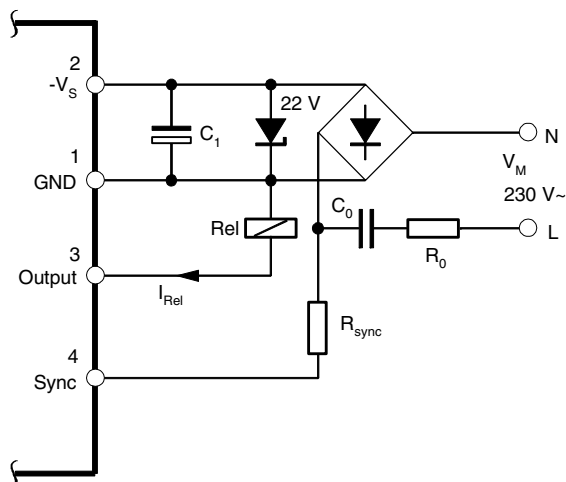
$$R_0 \approx 1/10 X_c$$

$$X_c = 0.85 \frac{V_M - V_S}{I_{\text{tot}}}$$

$$I_{\text{tot}} = I_S + I_{\text{Rel}} + I_X$$

$$C_0 = \frac{1}{\omega \times X_C}$$

Figure 6. DC Operation



$\alpha_{\min}$ Operation

Selection condition (Figure 7):

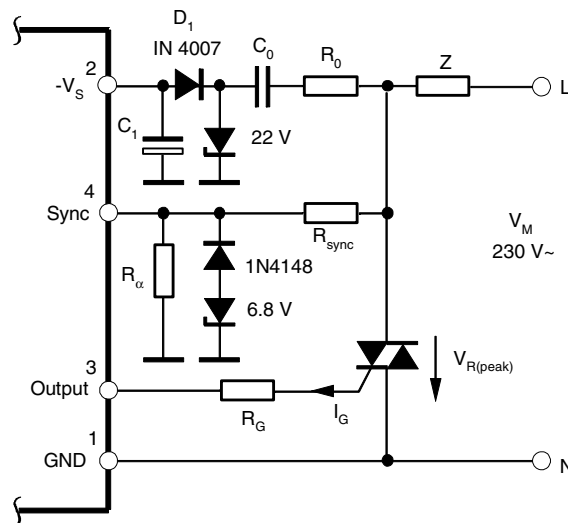
$$-V_4 = 6.5 \text{ to } 7.8 \text{ V} \quad +V_4 = \text{internal limitation}$$

$$R_{\alpha\max} = R_{\text{sync}} \frac{3.6 \text{ V}}{V_{R(\text{peak})} - 3.6 \text{ V}}$$

$$R_{\alpha\min} = R_{\text{sync}} \frac{10 \text{ V}}{V_M \times \sqrt{2} - 10 \text{ V}}$$

$V_{R(\text{peak})}$ is the peak voltage of the remaining phase angle, which should be high enough to generate the supply voltage, V_S .

Figure 7. $\alpha_{\min}$ Operation (Two-wire Operation)



$$C_1 = 100 \mu\text{F}/35 \text{ V}$$

$$C_0 = 0.33 \mu\text{F}/250 \text{ V} \sim$$

$$R_0 = 390 \Omega$$

$$R_{\text{sync}} = 220 \text{ k}\Omega$$

$$R_\alpha = 10 \text{ k}\Omega$$

$$R_G = 390 \Omega$$

$$D_1 = \text{IN } 4007$$

Tracking Time Pin 7

An internal RC oscillator with a following divider stage $1:2^{10}$ allows a very long and reproducible tracking time.

The RC values for the required final time, t_t , can be calculated as follows:

$$R_t[\Omega] = \frac{t_s[s] \times 10^6}{1.6 \times 1024 \times C_t[\mu F]}$$

$$C_t[\mu F] = \frac{t_s[s] \times 10^6}{1.6 \times 1024 \times R_t[\Omega]}$$

$$t_t[s] = \frac{C_t[\mu F] \times R_t[\Omega] \times 1.6 \times 1024}{10^6}$$

Trigger Inputs Pins 5 and 6

Two AND-connected, identical inputs determine the trigger conditions of the monostable time stages (Figure 8, Figure 9 on page 8), i.e., both inputs must be in position "ON" so that the output is switched on. The tracking time starts after the trigger conditions have elapsed. The output ON state is given until the tracking time has elapsed.

Input pin 5 is a simple comparator, whereas input pin 6 is designed as a window discriminator.

The noise suppression for $t_{ON} = 40$ ms prevents peak noise signals at the inputs which could trigger the circuit.

At the same time, the retrigger function is delayed for a duration of 640 ms (t_{OFF}) to avoid noise signal that may trigger the relay.

Figure 8. Trigger Condition, Pin 5

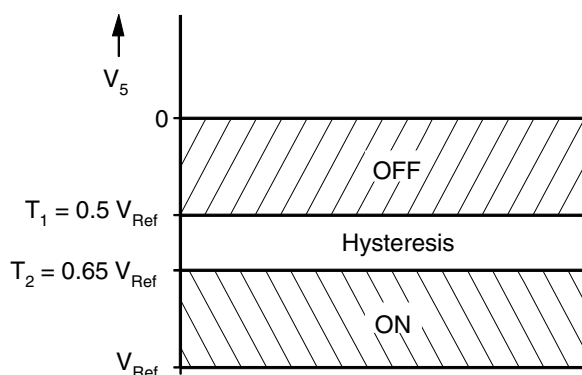
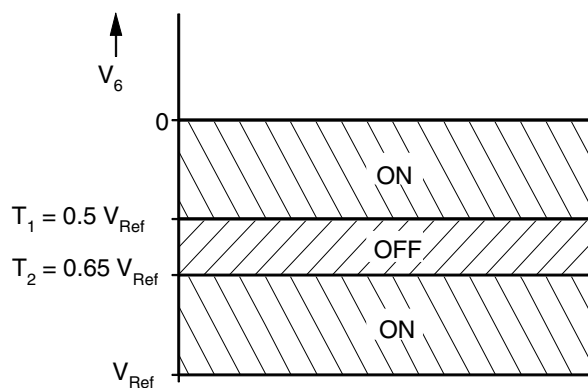


Figure 9. Trigger Condition, Pin 6



Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Pin	Symbol	Value	Unit
Supply				
Supply current	2	$-I_S$	10	mA
Peak current $t \leq 10$ ms		$-i_s$	60	mA
Supply voltage		$-V_S$	32	V
Reference voltage source	8	I_O	3	mA
Output current				
Synchronization	4	$\pm I_{Sync.}$	5	mA
Input current		$i_{Sync.}$	20	mA
$t \leq 10$ ms				
Window Monitoring				
Input voltage	6	$-V_1$	V_{Ref} to 0	V
Enable Schmitt Trigger				
Input voltage	5	$-V_1$	V_{Ref} to 0	V
Driver Output				
Collector voltage	3	$-V_o$	V_S to 2	V
Storage temperature range		T_{stg}	-40 to +125	°C
Junction temperature		T_j	125	°C
Ambient temperature range		T_{amb}	0 to 100	°C

Thermal Resistance

Parameters		Symbol	Value	Unit
Junction ambient	DIP8	R_{thJA}	110	K/W
	SO8 on PC board	R_{thJA}	220	K/W
	SO8 on ceramic	R_{thJA}	140	K/W

Electrical Characteristics

$V_S = -18\text{ V}$, $T_{\text{amb}} = 25^\circ\text{C}$, reference point pin 1, unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit
Supply-voltage limitation	I _S = 800 μA I _S = 2 mA	2	-V _S -V _S	21 21.3		23 24	V V
Current consumption	I ₃ = 0		-I _S			750	μA
Supply-voltage Monitoring		2					
ON-threshold			-V _S		15		V
OFF-threshold			-V _S		6.5		V
Reference voltage	I ₈ = 0.1 mA I ₈ = 1.5 mA	8	-V _{Ref} -V _{Ref}	4.95 4.75		5.45 5.45	V V
Synchronization		4					
Input current			±i _{sync}	0.1		1.1	mA
Voltage limitation	I ₄ = ±1 mA		±V _{sync}	8.8	9.4	10	V
Remaining phase angle	ON		±V _T	3.6	4	4.4	V
α _{min} threshold	Off		±V _T	1.8	2	2.2	V
Zero identification		4					
Zero identification	ON		±V _T		1.5		V
	OFF		±I _T		8.5		μA
			±V _T		4		V
			±I _T		20		μA
Operation Selection		4					
Zero voltage switch			±V _{sync}		V ₄ limit		
α _{min} operation			+V _{sync} -V _{sync}		V ₄ limit 6.5 to 7.8		V V
			-V _{sync} +V _{sync}		V ₄ limit 6.5 to 7.8		V V
Window Monitoring, Figure 9 on page 8		6					
Threshold 1			-V _I /V _{Ref}	0.52	0.49	0.46	
Threshold 2			-V _I /V _{Ref}	0.67	0.65	0.63	
Enable Schmitt Trigger, Figure 8 on page 7		5					
Threshold 1	OFF		-V _I /V _{Ref}	0.33	0.3	0.27	
Threshold 2	ON		-V _I /V _{Ref}	0.62	0.6	0.58	
Oscillator		5					
f = $\frac{1}{1.6 \times R_t \times C_t}$							
Threshold 1	7 - 1		V _I /V _{Ref}	0.25	0.20	0.15	
Threshold 2	7 - 8		V _I		100	200	mV
Input current	7		I _I		100	500	nA
Output Stage		3					
Saturation voltage	I ₃ = 100 mA		V ₃₋₂			2	V
Output current			I ₃	100			mA

Applications

Figure 10. Lamp Time Control 18 Seconds to 23 Minutes for Two-wire Systems

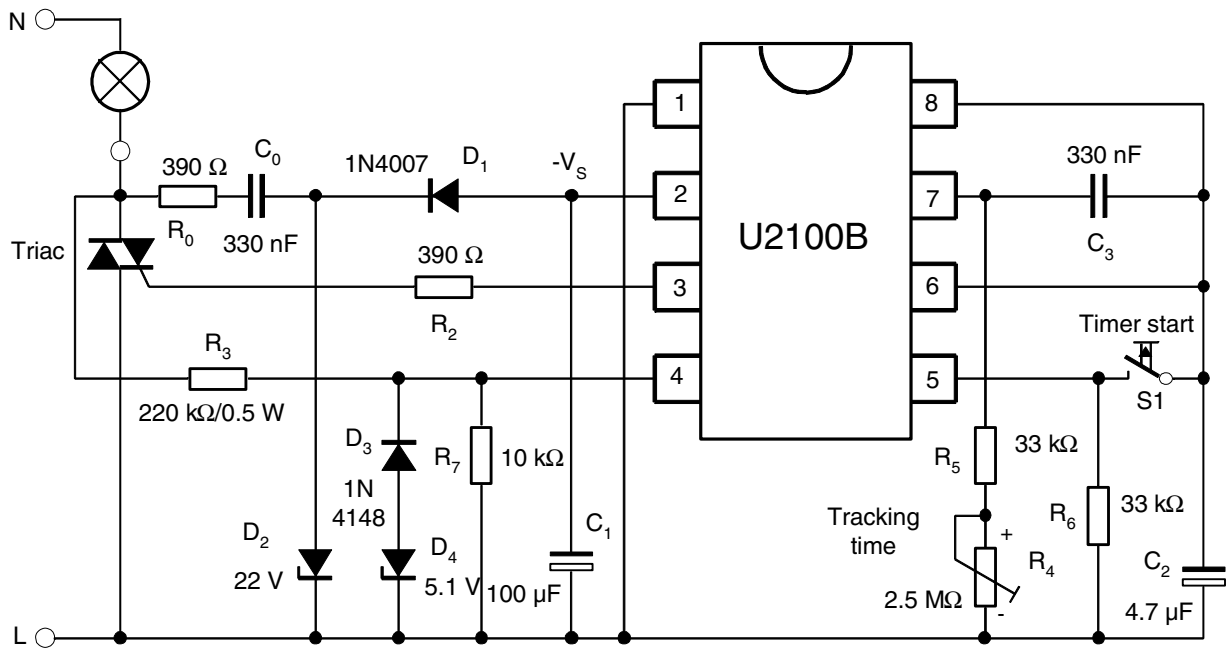
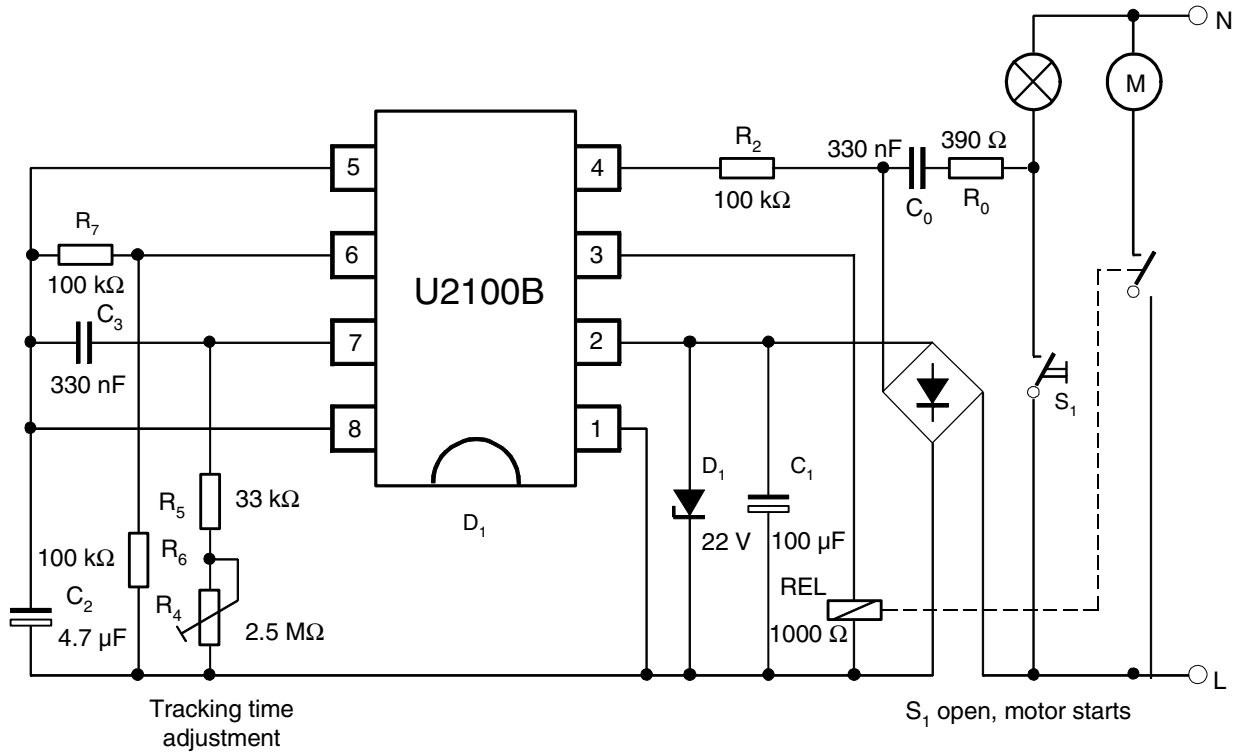


Figure 11. Fan Tracking Time Control 18 Seconds to 23 Minutes



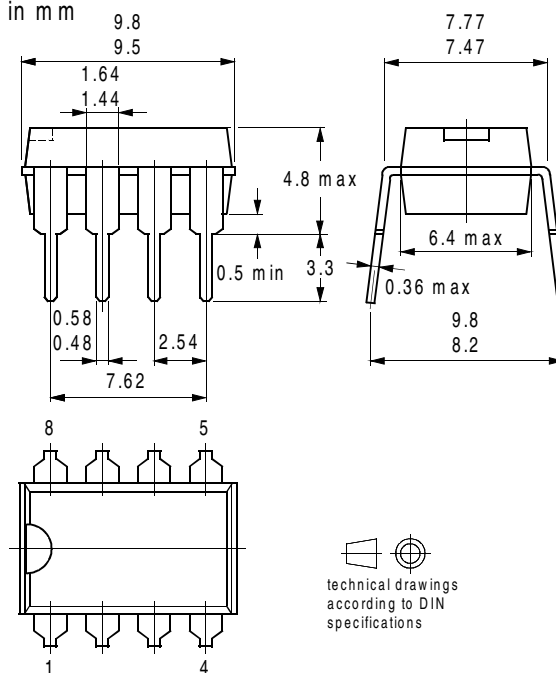
Ordering Information

Extended Type Number	Package	Remarks
U2100B-x	DIP8	Tube
U2100B-xFP	SO8	Tube
U2100B-xFPG3	SO8	Taped and reeled

Package Information

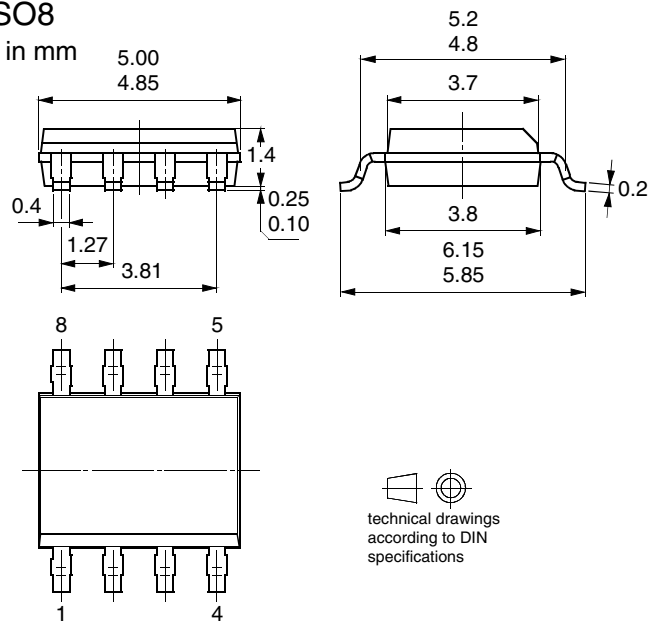
Package DIP8

Dimensions in mm



Package SO8

Dimensions in mm



Revision History

**Changes from Rev.
4769A - 10/03 to Rev.
4769B - 12/04**

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

1. Put datasheet in a new template.
2. Table "Electrical Characteristics" on page 9 changed.



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